

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the Application of:

Esin Terzioglu et al.

Serial No.: 10/824,905

Filed: April 15, 2004

For: MEMORY REDUNDANCE
CIRCUIT TECHNIQUES

Examiner: Son T. Dinh


Group Art Unit: 2824

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CERTIFICATE OF TRANSMISSION

I hereby certify that this correspondence is being transmitted via EFS-Web to the United States Patent and Trademark Office on December 19, 2006.



Michael T. Cruz
Reg. No. 44,636

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This paper responds to the Examiner's Statement of Reasons for Allowance ("the Examiner's Statement") in the above-identified application and accompanies the issue/publication fee transmittal (part B).